

Docket No.: 60188-126



Receipt
#1 #3
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Junichi YANO, et al.

Serial No.: 10/001,967

Filed: December 05, 2001

Group Art Unit:

Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**RESPONSE TO NOTICE
OF MISSING PARTS OF APPLICATION**

Box Missing Parts
Commissioner for Patents
Washington, DC 20231

Sir:

In response to the Notice of Missing Parts of Application dated January 30, 2002,
submitted herewith are the following documents for filing in the above-referenced application:

1. Verified English Translation
2. Formal Substitute Drawings
3. Information Disclosure Statement
4. Request for Corrected Filing Receipt
5. Late Filing Fee Surcharge of \$130.00

Please charge Deposit Account No. 500417 in the amount of \$130.00. To the extent
necessary, Applicants petition for an extension of time under 37 C.F.R. 1.136. Please charge any



20277

PATENT TRADEMARK OFFICE

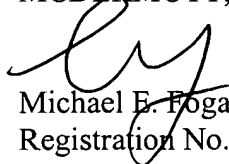
20010910 092600

10/001,967

shortage in fees due in connection with the filing of this paper, including extension of time fees,
to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
(202)756-8000 MEF:kjw
Facsimile: (202)756-8087
Date: March 26, 2002

20001967-030002



UNITED STATES PATENT AND TRADEMARK OFFICE



COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/001,967	12/05/2001	Junichi Yano	60188-126

CONFIRMATION NO. 1397

FORMALITIES LETTER



OC000000007388361

Jack Q. Lever, Jr.
McDERMOTT, WILL & EMERY
600 Thirteenth Street, N.W.
Washington, DC 20005-3096

Date Mailed: 01/30/2002

NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

04/29/2002 SSALEEKU 00000012 500417 10001967

FILED UNDER 37 CFR 1.53(b)

01 FC:139 130.00 CH

Filing Date Granted

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The application was filed in a language other than English. Applicant is required to provide an English translation of the specification and a statement that the translation is accurate. (See 37 CFR 1.52(d)).
- Applicant must file an English translation of the application, the \$ 130 fee set forth in 37 CFR 1.17(i), unless previously submitted, and a statement that the translation is accurate (37 CFR 1.52(d)).
- **The balance due by applicant is \$ 130.**
- Because your specification was filed in a language other than English, the Office was unable to determine the number of claims submitted. Additional claim fees may be due once the number of claims can be determined.

The application is informal since it does not comply with the regulations for the reason(s) indicated below.

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing figures contain text that is not in English (including, for example, a flow chart that was originally not in English that has been marked up to include the English text) see 37 CFR 1.84(p)(2) and 37 CFR 1.52(d)(1);

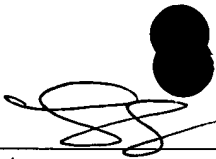
03/29/2002 NMOHAMM 00000033 500417 10001967

01 FC:105 130.00 CH

Adjustment date: 04/29/2002 SSALEEKU

03/29/2002 NMOHAMM 00000033 500417 10001967 A copy of this notice **MUST** be returned with the reply.

01 FC:105 130.00 CH



Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

20090426T0001



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Docket No.: 60188-126

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Junichi YANO, et al.

Serial No.: 10/001,967

Group Art Unit:

Filed: December 05, 2001

Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that foreign priority information is incorrect on the official filing receipt. Attached is a copy of the Declaration and Power of Attorney, which evidences that the foreign priority information should read: **Japanese Patent Application Number 2000-371299, Filed December 6, 2000**. Additionally, please correct the number of total claims to reflect: 16. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Michael E. Fogarty
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Date: March 26, 2002



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/001,967	12/05/2001		740	60188-126	9	116	1

CONFIRMATION NO. 1397

Jack Q. Lever, Jr.
McDERMOTT, WILL & EMERY
600 Thirteenth Street, N.W.
Washington, DC 20005-3096

RECEIVED

FEB 6 2002

McDermott, Will & Emery

FILING RECEIPT



OC000000007388360

Date Mailed: 01/30/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

Junichi Yano, Osaka, JAPAN; -
Hisato Yoshida, Nara, JAPAN; -
Kimihiko Aiba, Osaka, JAPAN; -
Katsuyuki Imamura, Osaka, JAPAN; -
Junichi Mori, Osaka, JAPAN; -
Junya Yamamoto, Kyoto, JAPAN; -

Assignment For Published Patent Application

MAITSUSHITA ELECTRIC INDUSTRIAL CO., LTD.; -

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-371299 06/12/2000 December 6, 2000

If Required, Foreign Filing License Granted 01/29/2002

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit

Preliminary Class

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

GRANTED

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Office of Export Administration, Department of Commerce (15 CFR 370.10 (j)); the Office of Foreign Assets Control, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).

#4.



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Junichi YANO, et al. :
Serial No.: 10/001,967 : Group Art Unit:
Filed: December 05, 2001 : Examiner:
For: SEMICONDUCTOR INTEGRATED CIRCUIT

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
Washington, DC 20231

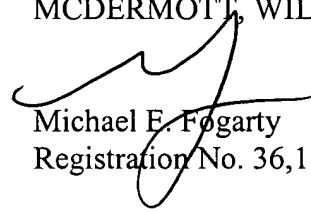
Sir:

At the time the above application was filed, informal drawings were presented with the application.

The formal drawings are submitted herewith.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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Registration No. 36,139

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